1SC0450V2Ax-45 and 1SC0450V2Ax-65 Target Datasheet

Single-Channel Cost-Effective SCALE™-2 IGBT Driver Core for 4500V and 6500V IGBTs

Abstract

The 1SC0450V2Ax-xx drives all usual high-power IGBT modules up to 4500V or 6500V. Thanks to its high output power capability, one single driver 1SC0450V2Ax-xx can drive up to four parallel connected 4500V or 6500V IGBT modules and consequently provides easy inverter design covering higher power ratings. Multi-level topologies involving 3300V or 4500V IGBTs with higher isolation requirements can also be easily supported by 1SC0450V2Ax-xx.

The 1SC0450V2Ax-xx combines a complete single-channel driver core with all components required for driving, such as an isolated DC/DC converter, short-circuit protection, Advanced Active Clamping as well as supply voltage monitoring. Enhanced features such as gate boosting or power supply short-circuit protection are also implemented and provide further driving benefits.

The driver's secondary side is electrically isolated from the primary side. The 1SC0450V2Ax-45 meets the requirements of 4500V IGBT applications while the 1SC0450V2Ax-65 covers the requirements of 6500V IGBT applications.

An output current of 50A and 6W drive power is available, making the 1SC0450V2Ax-xx an ideal driver platform for universal use in medium and high-power applications. The driver provides a gate voltage swing of 15V/-10V. The turn-on voltage is regulated to maintain a stable 15V regardless of the output power level.

Its outstanding EMC allows safe and reliable operation even in hard industrial applications.

Product Highlights

- ✓ Ultra-compact single-channel driver
- ✓ Highly integrated SCALE-2 chipset
- ✓ Gate current ±50A, 6W output power
- ✓ 15V/-10V gate driving
- ✓ Blocking voltages up to 4500V or 6500V
- ✓ Basic isolation to IEC 61800-5-1 and IEC 60664-1
- ✓ Short delay
- ✓ UL compliant
- ✓ RoHS compliant

Applications

- ✓ Traction
- ✓ Railroad power supplies
- ✓ Light rail vehicles
- ✓ HVDC
- ✓ Flexible AC transmission systems (FACTS)
- ✓ Medium-voltage converters
- ✓ Wind-power converters
- ✓ Industrial drives
- ✓ Medical applications

Target Data Sheet

Safety Notice!

The data contained in this data sheet is intended exclusively for technically trained staff. Handling all high-voltage equipment involves risk to life. Strict compliance with the respective safety regulations is mandatory!

Any handling of electronic devices is subject to the general specifications for protecting electrostatic-sensitive devices according to international standard IEC 60747-1, Chapter IX or European standard EN 100015 (i.e. the workplace, tools, etc. must comply with these standards). Otherwise, this product may be damaged.

Important Product Documentation

This data sheet contains only product-specific data. For a detailed description, must-read application notes and important information that apply to this product, please refer to "1SC0450V Description & Application Manual" on www.igbt-driver.com/go/1SC0450V

Mechanical Dimensions

Dimensions: See the "1SC0450V Description & Application Manual"

Fiber-Optic Interfaces

Interface	Remarks	Part type #
Drive signal input IN	Fiber-optic receiver (Notes 1, 2)	HFBR-2522ETZ
Status output OUT	Fiber-optic transmitter (Notes 1, 3)	HFBR-1522ETZ

Target Data Sheet

Absolute Maximum Ratings

Parameter	Remarks	Min	Max	Unit
Supply voltage V_{DC}	VDC to GND	0	16	V
Supply voltage V_{CC}	VCC to GND	0	16	V
Logic output voltage SO	To GND	-0.5	VCC+0.5	V
SO current	Failure condition, total current		20	mA
Gate peak current I_{out}	Note 4	-50	+50	A
External gate resistance	Turn-on and turn-off (Notes 4, 5)	0.3		Ω
Average supply current I_{DC}	Notes 6, 7		t.b.d.	mA
Output power	Ambient temperature ≤70°C (Notes 8, 9)		8	W
	Ambient temperature ≤85°C (Note 8)		6	W
Gate boost output power	Ambient temperature ≤70°C (Notes 8, 9)		t.b.d.	W
	Ambient temperature ≤85°C (Note 8)		t.b.d.	W
Switching frequency F			10	kHz
Power supply short circuit time	Non-repetitive (Note 25)		1	s
Test voltage (50Hz/1min.)	Primary to secondary (Note 18)		10.2	kV_{eff}
|dV/dt|	Rate of change of input to output voltage		50	kV/µs
Operating voltage	Primary/secondary, 1SC0450V2Ax-45		4500	V_{peak}
	Primary/secondary, 1SC0450V2Ax-65		6500	V_{peak}
Operating temperature	Notes 9	-40	+85	°C
Storage temperature		-40	+85	°C

Recommended Operating Conditions

Power Supply	Remarks	Min	Typ	Max	Unit
Supply voltage V_{DC}	VDC to GND	14.5	15	15.5	V
Supply voltage V_{CC}	VCC to GND	14.5	15	15.5	V

Electrical Characteristics (IGBT mode)

All data refer to +25°C and V_{CC} = V_{DC} = 15V unless otherwise specified.

Power supply	**Remarks**	**Min**	**Typ**	**Max**	**Unit**
Supply current I_{DC}	Without load		105		mA
Supply current I_{CC}			25		mA
Coupling capacitance C_{io}	Primary to output, total		8		pF
Supply voltage VGB	VGB to VISO (Note 26)		25		V
Maximum charge capability	VGB to VISO, no external capacitor (Note 27)		539		nC
Power Supply Monitoring	**Remarks**	**Min**	**Typ**	**Max**	**Unit**
Supply threshold V_{CC}	Primary side, clear fault	11.9	12.6	13.3	V
	Primary side, set fault (Note 15)	11.3	12.0	12.7	V
Monitoring hysteresis	Primary side, set/clear fault	0.35			V
Supply threshold V_{ISO}-V_E	Secondary side, clear fault	12.1	12.6	13.1	V
	Secondary side, set fault (Note 16)	11.5	12.0	12.5	V
Monitoring hysteresis	Secondary side, set/clear fault	0.35			V
Supply threshold V_E-V_{COM}	Secondary side, clear fault	5	5.15	5.3	V
	Secondary side, set fault (Note 16)	4.7	4.85	5	V
Monitoring hysteresis	Secondary side, set/clear fault	0.15			V
Power Supply Protection	**Remarks**	**Min**	**Typ**	**Max**	**Unit**
Short-circuit current threshold	VISO to COM, set fault (Note 23)		6.4		A
Fault feedback pulse	Fiber-optic OUT (Note 24)	500			µs
Logic Outputs	**Remarks**	**Min**	**Typ**	**Max**	**Unit**
SO pull-up resistor to VCC	On board		10		kΩ
SO output voltage	Failure condition, I(SO)<6.5mA			0.7	V
GBS	GBS to GH, in IGBT on state (Note 26)		7.6		V
	GBS to GH, in IGBT off state (Note 26)		0		V
Short-Circuit Protection	**Remarks**	**Min**	**Typ**	**Max**	**Unit**
V_{CE}-monitoring threshold	Factory set value (Note 21)		10.2		V
Minimum response time	Note 13		5.1		µs
Delay to clear fault state	After IGBT short circuit (Note 14)		9		µs
Delay in IGBT turn-off T_{cshd}	Factory-set value (Note 22)		0.2		µs

Timing Characteristics	Remarks	Min	Typ	Max	Unit
Turn-on delay $T_{d(on)}$	Note 10		120		ns
Turn-off delay $T_{d(off)}$	Note 10		100		ns
Output rise time $T_{r(out)}$	Note 11		t.b.d.		ns
Output fall time $T_{f(out)}$	Note 11		t.b.d.		ns
Transmission delay of fault state	Note 17		90		ns
Acknowledge delay time $T_{d(ack)}$	Note 28		220		ns
Acknowledge pulse width $T_{(ack)}$	On host side	400	700	1050	ns

Electrical Isolation	Remarks	Min	Typ	Max	Unit
Test voltage (50Hz/1s)	Primary to secondary side (Note 18)	10.2	10.3	10.4	kV_{eff}
Partial discharge extinction volt.	Primary to secondary side				
	1SC0450V2Ax-45 (Note 19)	5400			V_{peak}
	1SC0450V2Ax-65 (Note 19)	7800			V_{peak}
Creepage distance					
On the PCB	Primary to secondary side				
	(Material group IIIa)	45			mm
On the transformer	Primary to secondary side				
	(Material group I)	36			mm
Clearance distance	Primary to secondary side	25			mm

Output	Remarks	Min	Typ	Max	Unit
Blocking capacitance	VISO to VE (Note 12)		18.8		µF
	VE to COM (Note 12)		9.4		µF

Output voltage swing

The output voltage swing consists of two distinct segments. First, there is the turn-on voltage V_{GH} between pins GH and VE. V_{GH} is regulated and maintained at a constant level for all output power values and frequencies.
The second segment of the output voltage swing is the turn-off voltage V_{GL}. V_{GL} is measured between pins GL and VE. It is a negative voltage. It changes with the output power to accommodate the inevitable voltage drop across the internal DC/DC converter.

Output Voltage	Remarks	Min	Typ	Max	Unit
Turn-on voltage, V_{GH}	Any load condition		15.0		V
Turn-off voltage, V_{GL}	No load		-9.5		V
Turn-off voltage, V_{GL}	6W output power		-8.9		V
Turn-off voltage, V_{GL}	8W output power		-8.8		V

Footnotes to the Key Data

1) The transceivers required on the host controller side are not supplied with the gate driver. It is recommended to use the same types as used in the gate driver. For product information refer to www.IGBT-Driver.com/go/fiberoptics
2) The recommended transmitter current at the host controller is 20mA. A higher current may increase jitter or delay at turn-off.
3) The typical transmitter current at the gate driver is 20mA. In case of supply undervoltage, the minimum transmitter current at the gate driver is 17mA: this is suitable for adequate plastic optical fibers with a length of up to 10 meters.
4) The maximum peak gate current refers to the highest current level occurring during the product lifetime. It is an absolute value and does also apply for short pulses.
5) Two times the given minimum resistance value must be inserted between the interface connectors GH and GL. Moreover, the given minimal resistance value must be used in the full gate turn-on path (interface connector GH to gate) and in the full gate turn-off path (interface connector GL to gate).
6) The average supply input current is limited for thermal reasons. Higher values than specified by the absolute maximum rating are permissible (e.g. during power supply start up) if the average remains below the given value, provided the average is taken over a time period which is shorter than the thermal time constants of the driver in the application.
7) There is no protection against light power supply overload. In the case of start-up with very high blocking capacitor values, or in case of short circuit/heavy overload at the output, the supply input current is limited internally. The time during which the driver's output is shorted/overloaded has to be limited externally and has to be within the absolute maximum rating.
8) The maximum output power must not be exceeded at any time during operation. The absolute maximum rating must also be observed for time periods shorter than the thermal time constants of the driver in the application.
9) An extended output power range is specified in the output power section for ambient temperatures limited from -40°C to 70°C.
10) Including the delay of the external fiber-optic links (cable length: 1m). Measured from the transition of the turn-on or turn-off command at the optical transmitter on the host controller side to the direct output of the gate drive unit (excluding the delay of the gate resistors). The delay time is measured between 50% of the input signal and 10% voltage swing of the corresponding output. The delay time is independent on the output loading.
11) Output rise and fall times are measured between 10% and 90% of the nominal output swing with an output load of 4.7Ω and 270nF. The values are given for the driver side of the gate resistors. The time constant of the output load in conjunction with the present gate resistors leads to an additional delay at the load side of the gate resistors.
12) External blocking capacitors should be placed between the VISO and VE as well as the VE and COM terminals. Refer to "1SC0450V Description & Application Manual" (paragraph "DC/DC output (VISO), emitter (VE) and COM terminals)" for recommendations. Ceramic capacitors are recommended.
13) The minimum response time is valid for the circuit given in the description and application manual with the values of the corresponding tables.
14) Measured on the host side. The fault status on the secondary side is extended by the "delay in IGBT turn-off" and automatically reset after the specified time.
15) Undervoltage monitoring of the primary-side supply voltage (VCC to GND). If the voltage drops below this limit, a fault is transmitted to the SO output.
16) Undervoltage monitoring of the secondary-side supply voltage (VISO to VE and VE to COM, which correspond to the approximate turn-on and turn-off gate-emitter voltages). If the corresponding voltage drops below this limit, a fault is transmitted to the fiber optic output and the IGBT is switched off after the corresponding delay. Refer to "1SC0450V Description & Application Manual" for more details.
17) Delay of external fiber-optic links. Measured from the driver secondary side (ASIC output) to the optical receiver on the host controller.

18) HiPot testing (= dielectric testing) must generally be restricted to suitable components. This gate driver is suited for HiPot testing. Nevertheless, it is strongly recommended to limit the testing time to 1s slots. Excessive HiPot testing at voltages much higher than $3182V_{AC,rms}$ with 1SC0450V2Ax-45 and $4596V_{AC,rms}$ with 1SC0450V2Ax-65 may lead to insulation degradation. No degradation has been observed over 1min. testing at $10.2kV_{AC,rms}$. Every production sample shipped to customers has undergone 100% testing at the given value for 1s.
19) Partial discharge measurement is performed in accordance with IEC 60270 and IEC 60664-1 for basic insulation requirements.
20) Jitter measurements are performed with input signals INx switching between 0V and 15V referred to GND, with a corresponding rise time and fall time of 6ns.
21) The VCE–monitoring threshold value can be reduced with an external resistor. Refer to "1SC0450V Description & Application Manual".
22) The turn-off event of the IGBT after a secondary-side fault (IGBT short circuit, undervoltage monitoring or power-supply short circuit/heavy driver overload) can be additionally delayed with an external capacitor. Refer to "1SC0450V Description & Application Manual".
23) Gate turn-on and turn-off current pulses in normal operation will not affect the power supply protection. Power supply protection will only be triggered by corresponding power-supply overload or short circuit (which would also occur in case of gate-emitter short circuit/overload).
24) The fault feedback pulse length/pattern depends on the available power supply short-circuit/overload. The minimum value applies for any power supply overload.
25) Maximum short-circuit duration of the driver output. The driver's power supply VDC has to be switched off externally within the given time. The power supply protection prevents the driver's components to be damaged within the given time frame. For details see the driver's "1SC0450V Description & Application Manual".
26) The voltage values of the pins VGB resp. GBS are maximum 50V resp. 35V referred to COM. This must be considered for the design of the creepage and clearance distances.
27) The given value applies for a full discharge of VGB to VISO. It can be increased using external capacitors.
28) Including the delay of the external fiber-optic links (cable length: 1m). Measured from the transition of the turn-on or turn-off command at the optical transmitter on the host controller side to the transition of the acknowledge signal at the optical receiver on the host controller side.

Legal Disclaimer

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Ordering Information

The general terms and conditions of delivery of CT-Concept Technologie GmbH apply.

Type Designation	Description
1SC0450V2A0-45	Single-channel 4.5kV SCALE-2 driver core
1SC0450V2A0-65	Single-channel 6.5kV SCALE-2 driver core

Product home page: www.IGBT-Driver.com/go/1SC0450V

Refer to www.IGBT-Driver.com/go/nomenclature for information on driver nomenclature

Information about Other Products

For other drivers, product documentation, and application support

Please click: www.IGBT-Driver.com

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